

#9/ste-12
PATENT
0465-0751P
Drawing
9/20/02

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Ha Z. KIM Conf.: 2187
Appl. No.: 09/820,217 Group: 2822
Filed: March 29, 2001 Examiner: M. GUERRERO
For: SEMICONDUCTOR DEVICE AND METHOD FOR
MANUFACTURING THE SAME

LETTER TO THE OFFICIAL DRAFTSPERSON

Assistant Commissioner of Patents
Washington, D.C. 20231

September 17, 2002

Sir:

The Examiner is respectfully requested to approve changes to Figure 1 as shown in red ink on the attached drawing sheet. In particular, Figure 1 is being amended to show the current path 7 passing through the n-type well 4, which is supported by the specification on page 2, lines 3-5. Consideration and approval of the proposed drawing corrections are respectfully requested.

It is requested that the requirement for corrected formal drawings be held in abeyance until allowance of this application.

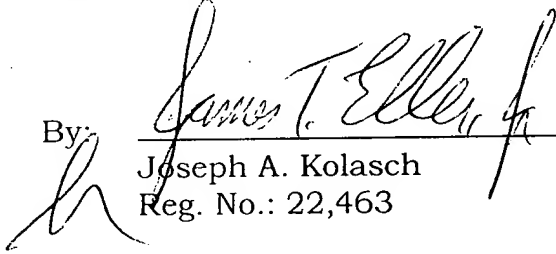
If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit

Account No. 02-2448 for any additional fees required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By:


Joseph A. Kolasch
Reg. No.: 22,463

#39,538

JAK:sld

P.O. Box 747
Falls Church, Virginia 22040-0747
Telephone: (703)205-8000

Attachment: Figure 1